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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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Application / Conf. No.	10/618,404 /	5533
Filing Date	July 11, 2003	
First Named Inventor	Steven P. Young	
Art Unit	2819	
Examiner Name	James Hyonchol Cho	
Attorney Docket Number	X-1392 US	

			U.S. PATENT	DOCUMENTS	
Examiner Initials	Cite No.'	Document Number Number - Kind Code (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
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		TION DISCLOSURE	First Named Invent r	Steven P. Young				
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Sheet	1	of	2	Attorney Docket Number	X-1392 US			

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Examiner	. Cite	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
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	Sheet	2	of	2	Attorney Docket Number	X-1392 US			

		OTHER - NON PATENT LITERATURE DOCUMENTS	
xaminer Initials	Cite No¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-Issue number(s), publisher, city and/or country where published.	त
X	/	Xilinx, Inc.; DS083-2 (v2.7) Advance Product Specification; "Virtex-II Pro Platform FPGAs: Functional Description"; available from Xilinx, Inc., 2100 Logic Drive, San Jose, California 95124; pp. 1-48.	
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